

### **CMOS Smart Camera**

# SM2-D1312 SERIES



## 1.4 Megapixel smart camera with programmable DSP

### **Features**

- Photonfocus A1312 CMOS image sensor
- 1312 x 1082 pixel resolution
- Programmable Texas Instruments DSP
- HALCON Embedded compatible
- Exceptional SNR up to 300:1
- Dynamic range up to 120 dB via LinLog®
- Up to 108 fps @ full resolution
- JPEG compression @ 68 fps<sup>(1)</sup>
- Global shutter
- Monochrome
- GigE interface
- 12 bit greyscale resolution

Compatible with



### Spectral response of the Photonfocus A1312 CMOS image sensor







		Image sensor		
Image sensor	Photonfocus A1312 (3. Generation)			
Technology	CMOS active pixel (APS)			
Scanning system	Progressive scan			
Optical format / diagonal	1" (13.6 mm diagonal) maximum resolution			
	2/3" (11.6 mm diagonal) 1024 x 1024 resolution			
Resolution	1312 x 1082 pixels			
Pixel size	8 µm x 8 µm			
Active optical area	10.48 mm x 8.64 mm (maximum)			
Dark current	0.65 fA/pixel			
ull well capacity / SNR	~90 ke / 300:1			
Spectral range	< 370 to 1000 nm (to 10 % of peak responsivity)			
Responsivity	210 x 10 <sup>3</sup> DN / (J/m²) @ 625 nm / 8 bit / gain = 1			
nesponsivity	(approximately 620 DN / (lux s) @ 625 nm / 8 bit / gain = 1)			
Quantum Efficiency	(аррголин	> 50 %		
Optical fill factor	> 60 %			
Dynamic range	60 dB in linear mode; 120 dB with LinLog®			
Colour format	Monochrome			
Characteristic curve	Linear, LinLog®			
Shutter mode	Global shutter			
Read out mode	Sequential or simultaneous read out			
tead out mode	(read out during exposure only in linear mode) for higher frame rates			
	(read out dulii)	ig exposure only in inlear mode/ for mgr	iei itaitie tates	
		Camera		
xposure time	10 μs 0.83 s / 50 ns steps	10 μs 0.67 s / 40 ns steps	10 μs 0.41 s / 25 ns steps	
rame rate	55 fps	68 fps	108 fps	
ixel clock	40 MHz	50 MHz	80 MHz	
Camera taps		2 (internal)		
Greyscale resolution	8 bit / 10 bit / 12 bit 8 bit			
ixed pattern noise (FPN)	< 1 DN RMS @ 8 bit / gain = 1 / offset correction ON			
Analogue gain				
Digital gain	1/2/4/8			
Onfiguration interface	Built-in Webserver			
rigger modes	Free running (non triggered)    DSP controlled trigger    External trigger input			
eatures	Region of Interest (ROI) • 512 Multiple ROI (MROI) • Decimation Y • Image correction • 2 Look-up tables (LUT)			
reatures	• Constant frame rate • Crosshair • Convolver 3x3 • Temperature • Image information • Realtime clock			
	IPEG Compression(1) • E	<ul> <li>JPEG Compression<sup>(1)</sup></li> <li>FTP Server</li> <li>Extended trigger input and strobe output functionality</li> </ul>		
PU / RAM / Storage	Texas Instruments TMS320 C6415 @ 1GHz, 8000 MIPS / 256 MB SDRAM / 2 GB SD Card <sup>22</sup>			
nterface	GiaE			
Operating temperature	0°C +50°C			
Power supply	+12 V DC (±10%)			
ower consumption	10 W			
ens mount	C-Mount (CS-Mount optional)			
Dimensions (H x W x L)	60 x 60 x 137 mm <sup>3</sup>			
	572 a			
Mass	CF / RoHS / WFFF			
onformity	CE / ROHS / WEEE  Adjustable backfocus; Opto-isolated I/Os; JTAG, RS232 Interface, RS422 Interface			
Specials	Adjustable backfocus	s; Upto-isolated I/Os; JTAG, RS232 Interf	ace, KS422 Interface	
	Software			
Camera control	Built-in webserver			
DSP Development tools	Texas Instruments Code Composer Studio 3.3, HALCON Embedded			
	icad instrum	2222 Composer Stadio 3.3, TIMECC		

<sup>\*</sup> Product name used by Imago Technologies

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<sup>(</sup>I) Feature only available for SMZ-D1312-JPEG-100-GB-12 camera (for other cameras on request) (II) DSP TI TMS320 C6455 @ 1.2GHz, 9600 MIPS / 512 MB SDRAM (available on request)